DISPLAY CONTROLLER AND RELATED METHOD FOR CALIBRATING DISPLAY DRIVING VOLTAGES ACCORDING TO INPUT RESISTANCE OF A MONITOR

Appl. No.

10/708,638

Confirmation No. 2637

Applicant

Chi-Yang Lin,

Peter Chen

Filed

March 17, 2004

TC/A.U.

2629

Examiner

PIZIALI, JEFFREY J

Docket No.

VIAP0101USA0

Customer No.

27765

Commissioner for Patents

P.O. Box 1450

Alexandria VA 22313-1450

5

AMENDMENT

Sir:

In response to the Office action of September 22, 2006, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.